

Patent Abstracts of Japan

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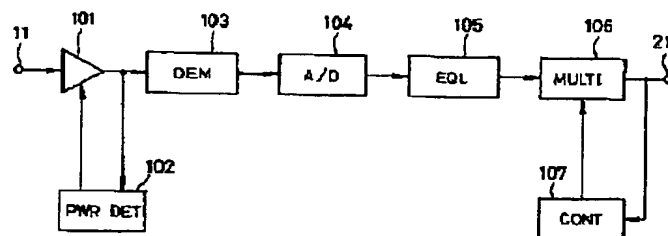
APPLICATION DATE : 15-12-93
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TITLE : DEMODULATOR



ABSTRACT : PURPOSE: To prevent an error rate from being degraded at normal time with not interference component and to obtain the maximum ability of an equalizer.

CONSTITUTION: An analog received signal containing the interference wave component is converted into a digital signal by an A/D converter 104. The amplitude of the analog received signal is controlled by a variable gain amplifier 101 and a maximum amplitude detector 102 so that its maximum value can match the maximum convertible range of the A/D converter 104. At an equalizer 105, the interference component is removed from the converted output of the A/D converter, and the signal after this interference component removal is corrected by a multiplier 106 and a control circuit 107 so that the maximum value of the analog received signal between the convergent points of an eye pattern can match a prescribed reference value. Thus, since it is not necessary to reduce the amplitude of the signal at normal time, the error rate is not degraded, and the maximum ability of the equalizer can be obtained.

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